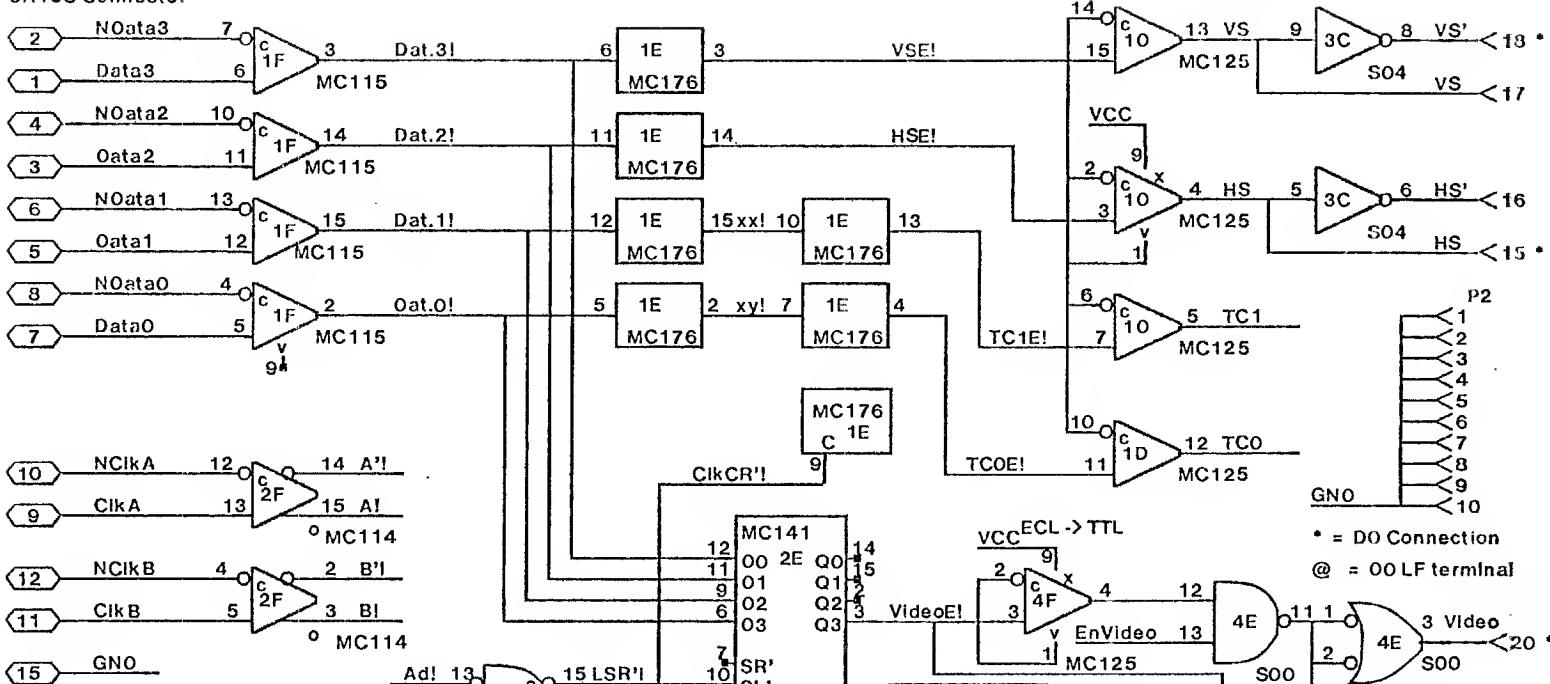


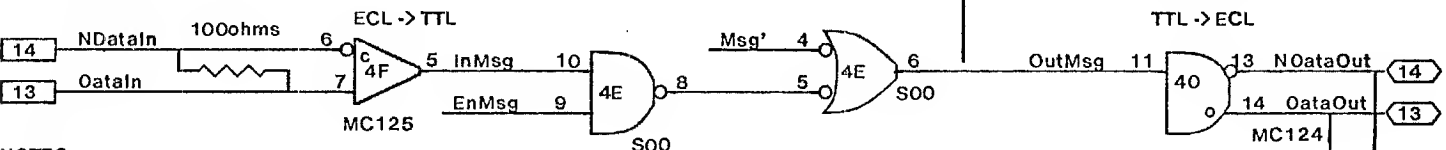
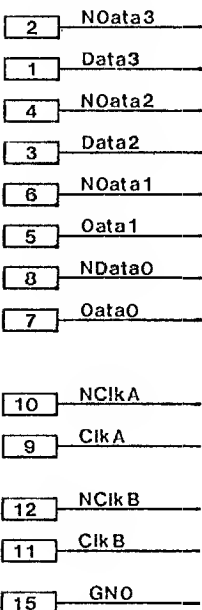
J1

0A15S Connector



P1

DA15P Connector



## NOTES:

1) All signals with names of the form xxx! have 510ohm pulldown resistors to VEE (-5.2)  
(26 places)

2) P2 consists of wire-wrap posts on .100 centers

3) Ground, VCC, and VEE pins for ECL chips are as shown:

	GND	VEE	VCC
MC102, MC105, MC114, MC115	1,16	8	-
MC141	1,15,16	8	-
MC111	16	8	9
MC124, MC125	16	8	-
MC173, 176	16	8	-

XEROX  
PARCProject  
DO7-Wire Terminal Interface  
Video InterfaceFile  
Terminal02.silDesigner  
ThackerRev  
GbDate  
12/26/79Page  
2

